

Docket No.: 394-1969

2615
PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#2
Rose
5-15-96

In re Application of

Akihiko TAKABATAKE et al.

Serial No. 08/594,195

Group Art Unit: 2104

Filed: January 31, 1996

Examiner: TBD

For: PICTURE DECODING AND DISPLAY UNIT INCLUDING A MEMORY
HAVING REDUCE STORAGE CAPACITY FOR STORING PIXEL DATA

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner of
Patents and Trademarks
Washington, D. C. 20231

RECEIVED

APR 30 1996

96 MAY 13 AM10:22
GROUP 260

RECEIVED

Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

The article in "Nikkei Electronics" is discussed in the Background Art Section of the specification.

The article in "SGS-THOMPSON MICROELECTRONICS" discloses a B-frame decoding procedure, wherein the start of displaying a B-frame is delayed by one frame duration with respect to the start of decoding the B-frame.

The partial translation of the article in "Proceedings of the 1995 IEICE General Conference" written by the inventors, together with co-authors, are provided herewith.

Respectfully submitted,

LOWE, PRICE, LEBLANC & BECKER



Alexander V. Yampolsky
Registration No. 36,324

99 Canal Center Plaza, Suite 300
Alexandria, Virginia 22314
(703) 684-1111 AVY:cs:as
Date: April 25, 1996